

TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT
(Under 37 CFR 1.97(b) or 1.97(e))

Docket No.
FIS920010377US1

In Re Application Of: Towler et al.

DEC 23 2002

Serial No.
10/065,842

Filing Date
11/25/02

Examiner

Group Art Unit

Title: CONTENT ADDRESSABLE MEMORY WITH PFET PASSGATE SRAM CELLS

Address to:
Assistant Commissioner for Patents
Washington, D.C. 20231

37 CFR 1.97(b)

1. ☒ The Information Disclosure Statement submitted herewith is being filed within three months of the filing of a national application other than a continued prosecution application under 37 CFR 1.53(d); within three months of the date of entry of the national stage as set forth in 37 CFR 1.491 in an international application; before the mailing of a first Office Action on the merits, or before the mailing of a first Office Action after the filing of a request for continued examination under 37 CFR 1.114.

37 CFR 1.97(c)

2. ☐ The Information Disclosure Statement submitted herewith is being filed after the period specified in 37 CFR 1.97(b), provided that the Information Disclosure Statement is filed before the mailing date of a Final Action under 37 CFR 1.113, a Notice of Allowance under 37 CFR 1.311, or an Action that otherwise closes prosecution in the application, and is accompanied by one of:
- ☐ the statement specified in 37 CFR 1.97(e);
- OR
- ☐ the fee set forth in 37 CFR 1.17(p).

**INFORMATION DISCLOSURE CITATION***(Use several sheets if necessary)*

Docket Number (Optional)

FIS92001037/US1

Application Number

10/065,842

Applicant(s)

Towler et al.

Filing Date

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Group Art Unit

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
							YES	NO

OTHER DOCUMENTS *(Including Author, Title, Date, Pertinent Pages, Etc.)*

		IBM Technical Disclosure Bulletin, Bertin et al., "STACKED DENSE CMOS RAM USING SILICON ON INSULATOR TECHNOLOGY", Vol. 27, No. 6, November 1984, pp. 3492 - 3493.

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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CONTENT ADDRESSABLE MEMORY WITH PFET PASSGATE SRAM CELLS**Payment of Fee**

(Only complete if Applicant elects to pay the fee set forth in 37 CFR 1.17(p))

- ☐ A check in the amount of _____ is attached.
- ☒ The Assistant Commissioner is hereby authorized to charge and credit Deposit Account No.09-0456 as described below. A duplicate copy of this sheet is enclosed.
- ☐ Charge the amount of _____
- ☐ Credit any overpayment.
- ☐ Charge any additional fee required.

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I certify that this document and authorization to charge deposit account is being facsimile transmitted to the United States Patent and Trademark Office (Fax No. _____) on _____

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Signature

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Certificate of Mailing by First Class Mail

I certify that this document and fee is being deposited on 12/16/02 with the U.S. Postal Service as first class mail under 37 C.F.R. 1.8 and is addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231.

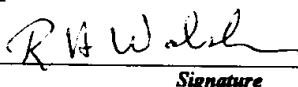


Signature of Person Mailing Correspondence

Pat Blair

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Signature

Dated: 12/12/2002

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